**Collar Board Schematic Notes**

Main Board:

* 1.1V Converter
* BUCK Converter With Boost
* CLOCK
* CPLD
* FLASH
* FPGA
* uUSB 3.0
* MRAM (may be moved)
* Octal Buffer
* Real Time Clock (may be moved)
* SDRAM

Daughter Board:

* Inertial Module 12 Lines
* GPS 5 Lines
* Microphones 3 Lines (May not be on board)
* SD Card 8 Lines
* Data Transfer 6 Lines
* Solar Controller 2 Lines (Update Schematic appropriately)
* Battery Monitor ~5 Lines (Update Schematic appropriately)

*1/2/15: Replaced feedback resistors with 0402 equivalents*

*1/9/15: Replaced all but 1.1V feedback resistors and FPGA capacitors with 0402 equivalents. Made CPLD decoupling caps 0201’s. Updated components to have Digi-Key ordering number.*